

<b>Notice of References Cited</b>		Application/Control No. 09/866,800	Applicant(s)/Patent Under Reexamination OKAWA ET AL.	
Examiner Johannes P Mondt		Art Unit 2826	Page 1 of 1	

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,744,841	04-1998	Gilbert et al.	257/360
	B	US-5,920,774	07-1999	Wu	438/224
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages
	U	S. Wolf and R.N. Tauber, "Silicon Processing for the VLSI Era", Volume 1 - Process Technology, pages 397-399, Lattice Press, Sunset Beach, California (USA), 1986 edition (ISBN 0-961672-3-7).
	V	S. Wolf, "Silicon Processing for the VLSI Era", Volume 3 - The Submicron MOSFET, pages 523-527, Lattice Press, Sunset Beach, California (USA), 1995 edition (ISBN 0-961672-5-3).
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.